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INTEGRATED RECEIVER DECODER FOR RECEIVING DIGITALLY MODULATED SIGNALS FROM A SATELLITE

An integrated receiver decoder for receiving digitally modulated signals from a satellite is disclosed. The receiver includes a tuner, a demodulator, a low-noise block (LNB) controller, a voltage controller and a voltage selector implemented within a single monolithic integrated circuit device. The tuner amplifies and filters satellite signals received from a directional receiver antenna. The demodulator, which is coupled to the tuner, demodulates and decodes the received satellite signals. The LNB controller generates and detects a modulated tone to facilitate communications between the receiver and an LNB feed attached to the directional receiver antenna. The voltage selector directs the voltage controller to provide a control signal for controlling an external voltage regulator to generate a variable voltage to the LNB feed attached to the directional receiver antenna.